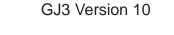
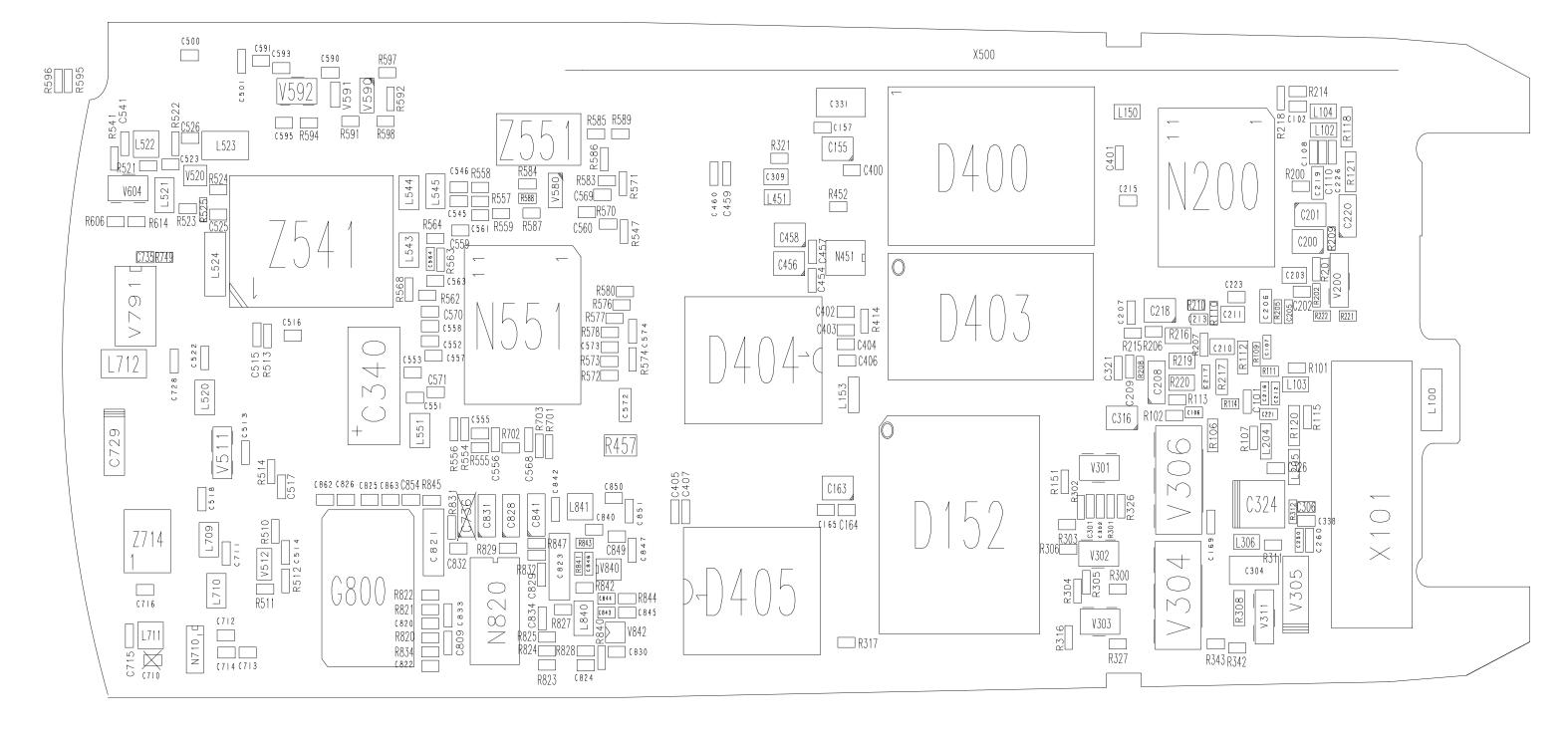
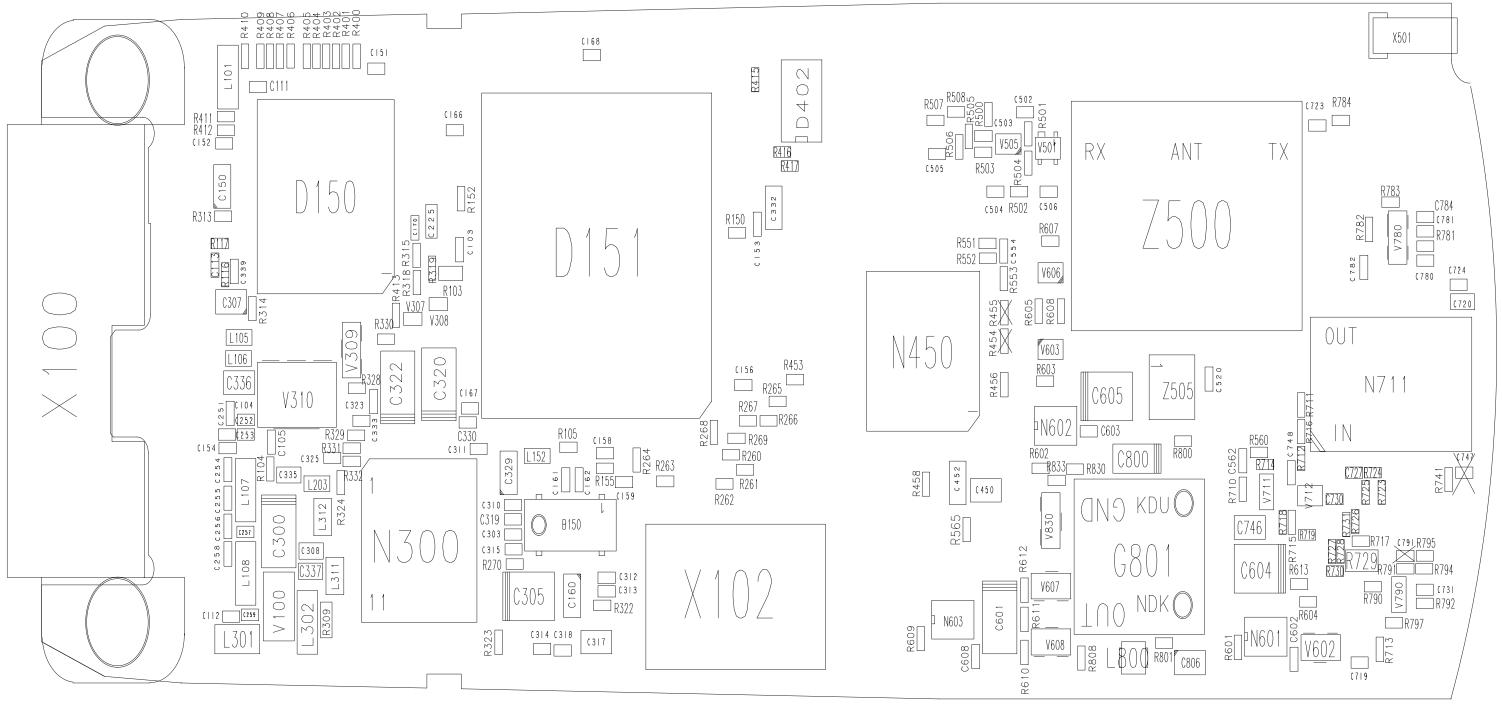
#### Figure 24 Component Layout Top



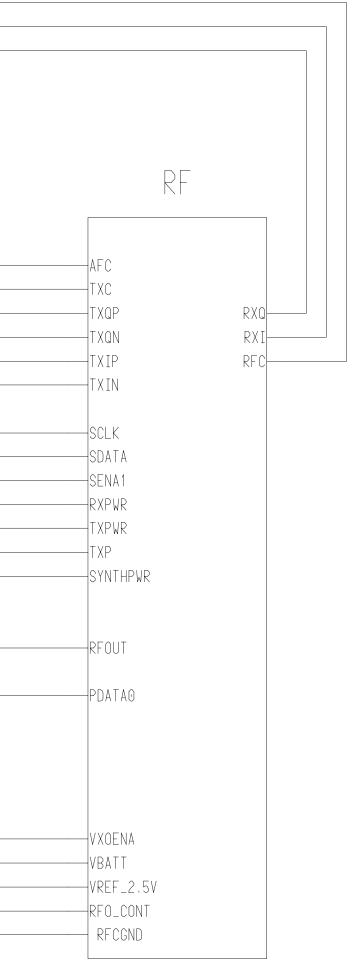




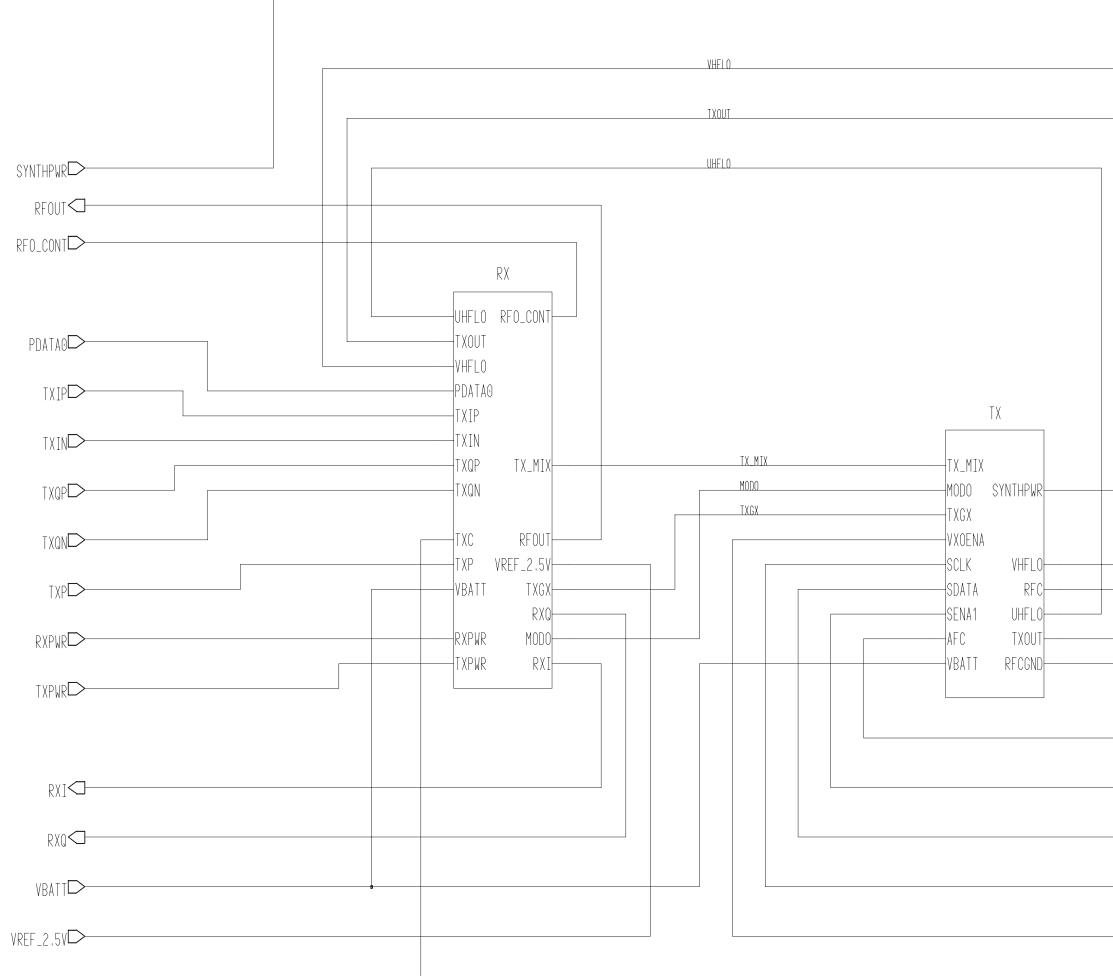


# Figure 26 System Block Diagram

Figure 26 System Block Diag	Iram	RFC RXI
		RXQ
SY	Ś	
RXQ RXI		
RFC		AFC TXC TXQP
	T X Q P T X Q N T X I P	TXQN TXIP
	TXIN	SCI K
	SCLK SDATA SENA1	SDATA SENA1
	RXPWR TXPWR	TXPWR TXPWR
	TXP	TXP Synthpwr
	RFOUT	
	PDATAO	PDATAO
	VXOENA	VBATT
	VBATT VREF_2.5V	
	RFO_CONT	VXOENA VBATT VREF_2.5V

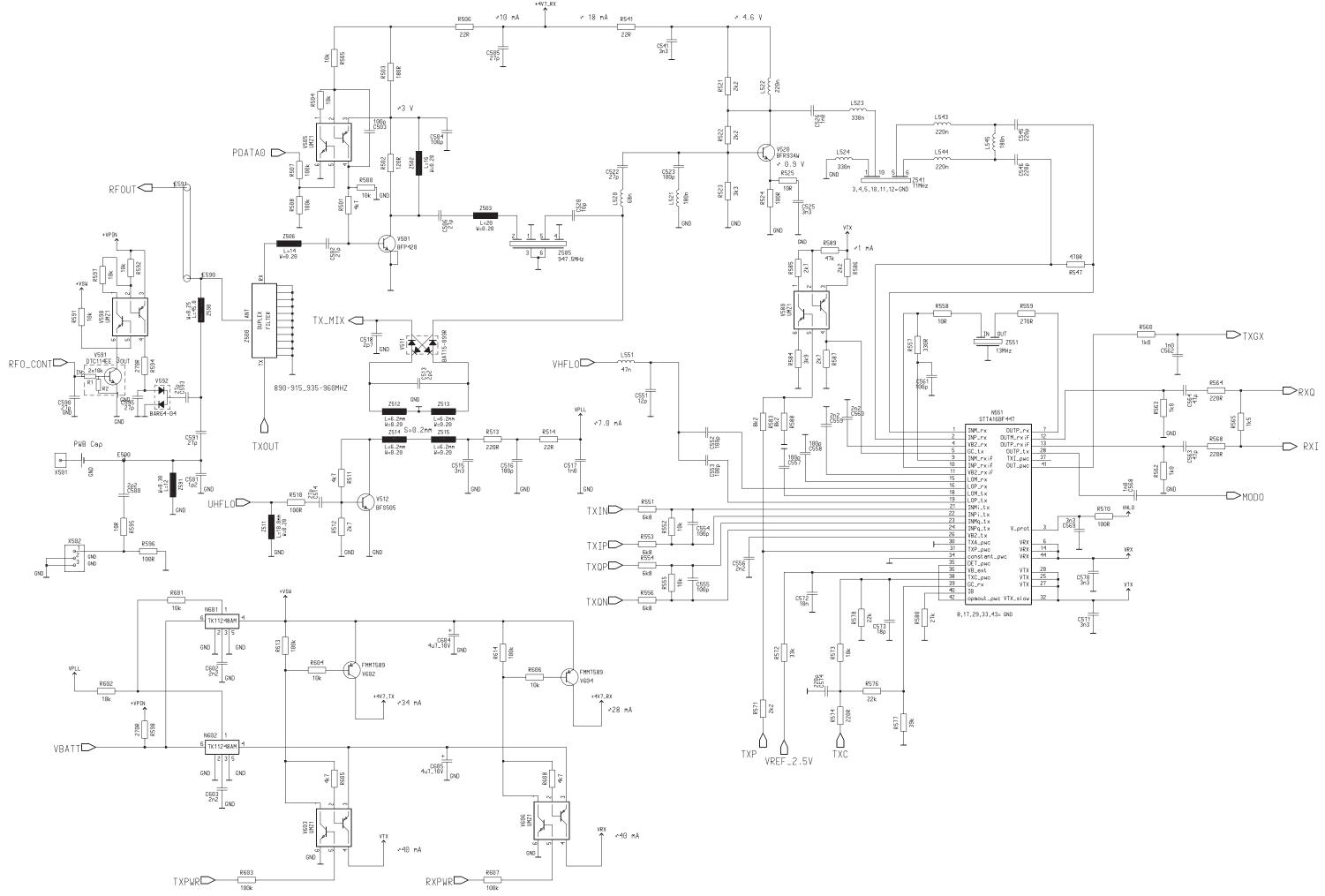


## Figure 27 RX/TX Block Diagram



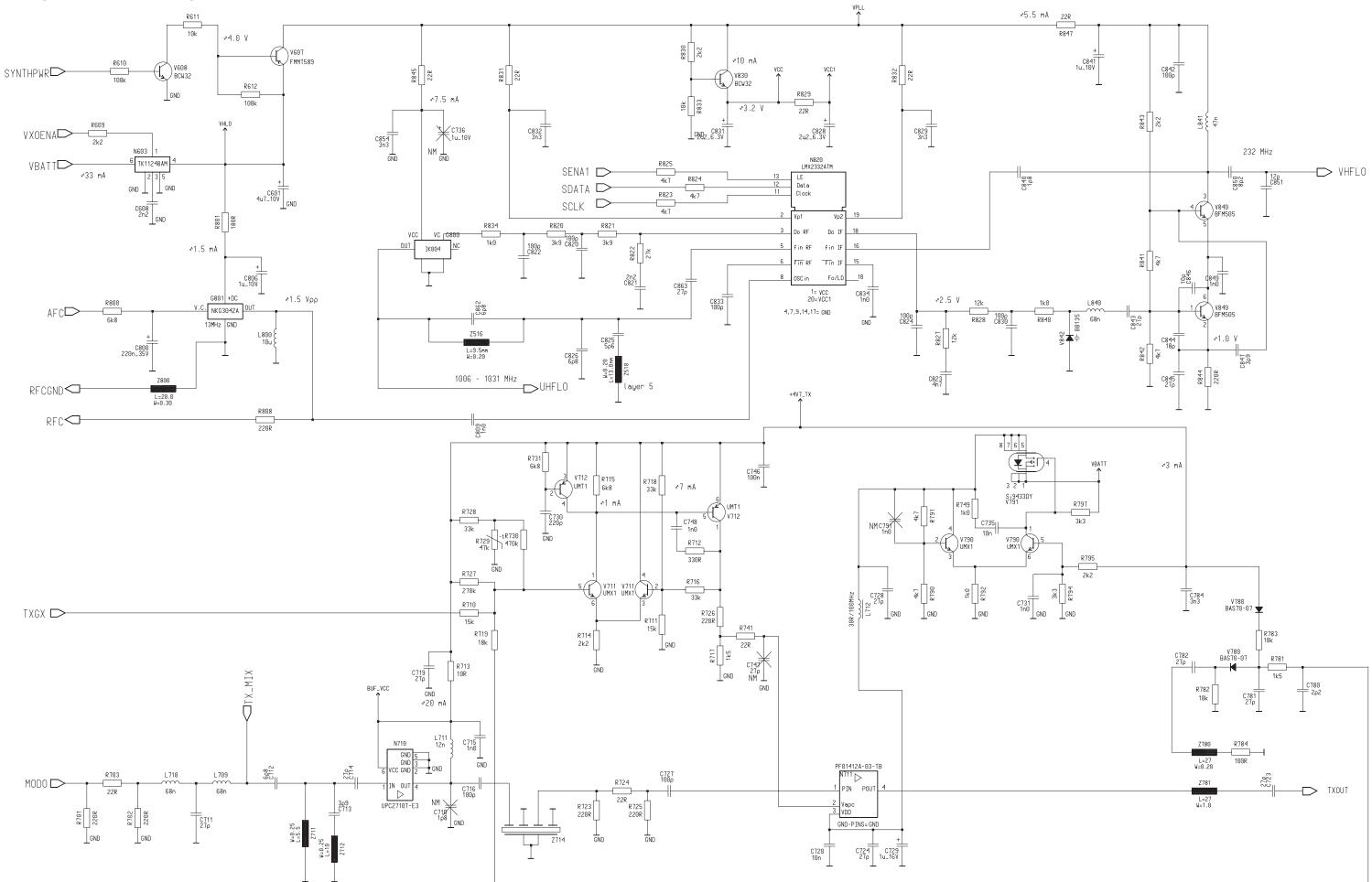
NHE 8/9
RFCGND
SENA1
SDATA
 SCLK
 ۲۸۵ Page3–A19

## Figure 28 RX Circuit Diagram

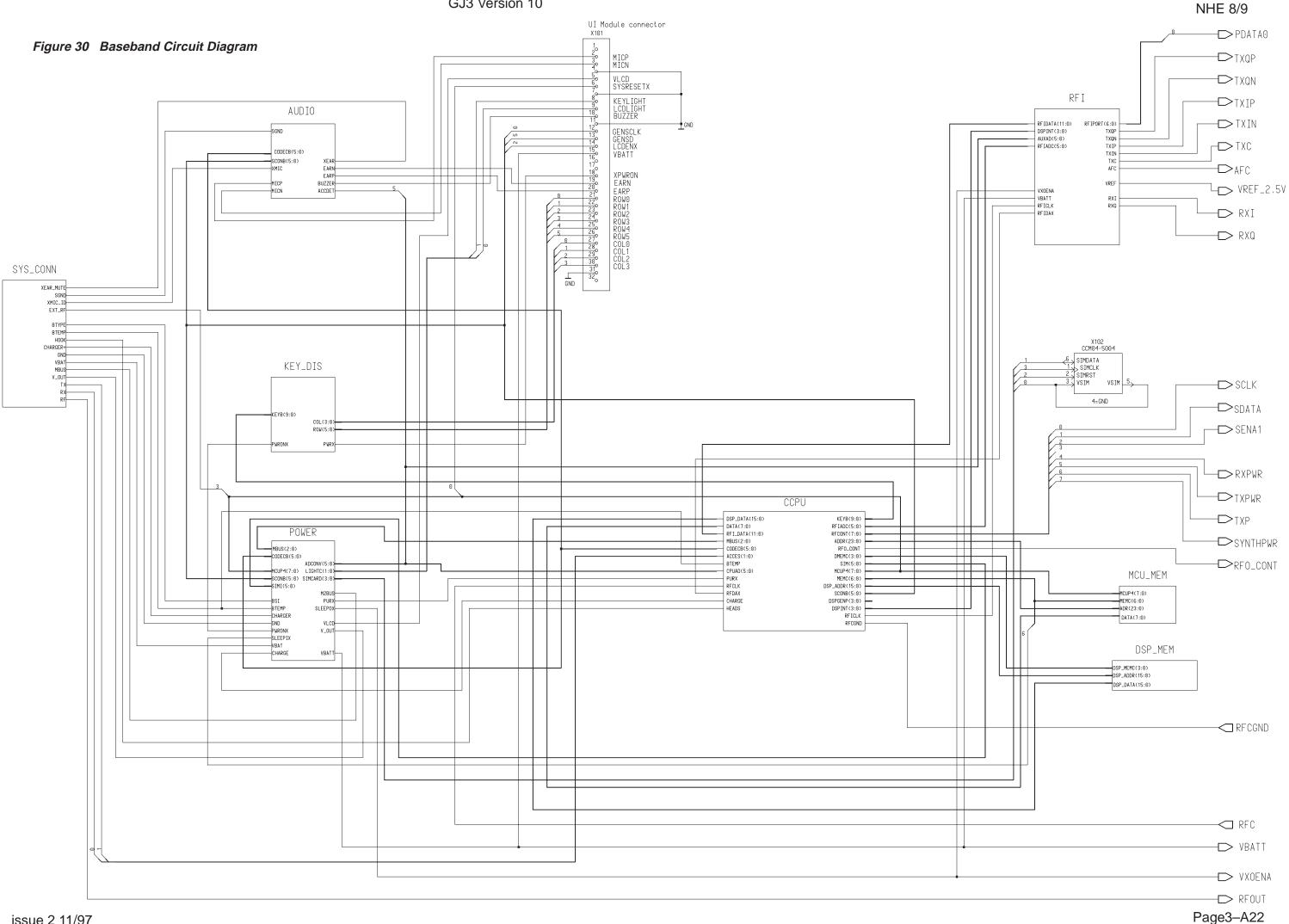


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### Figure 29 TX Circuit Diagram



NM = NOT MOUNTED

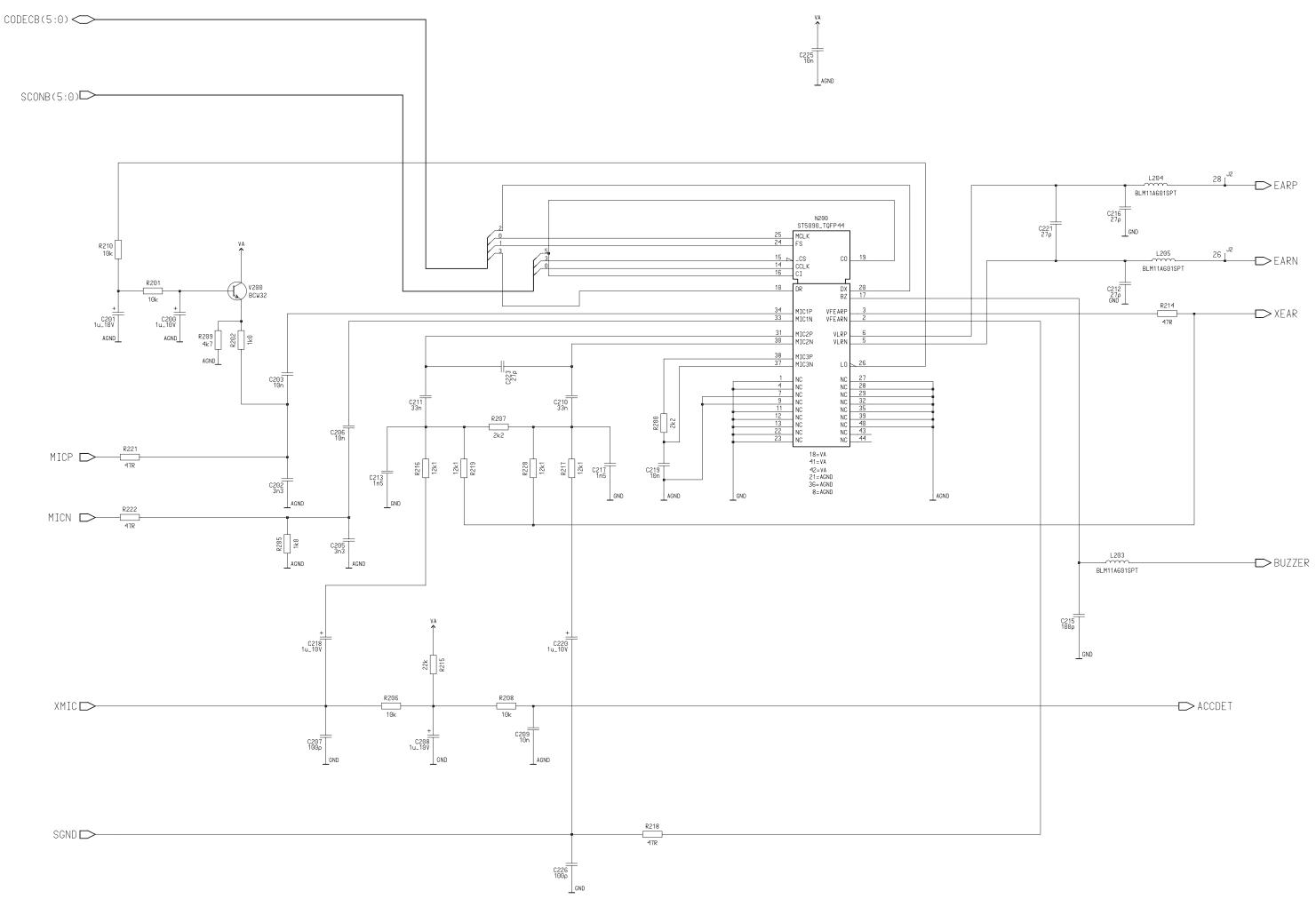


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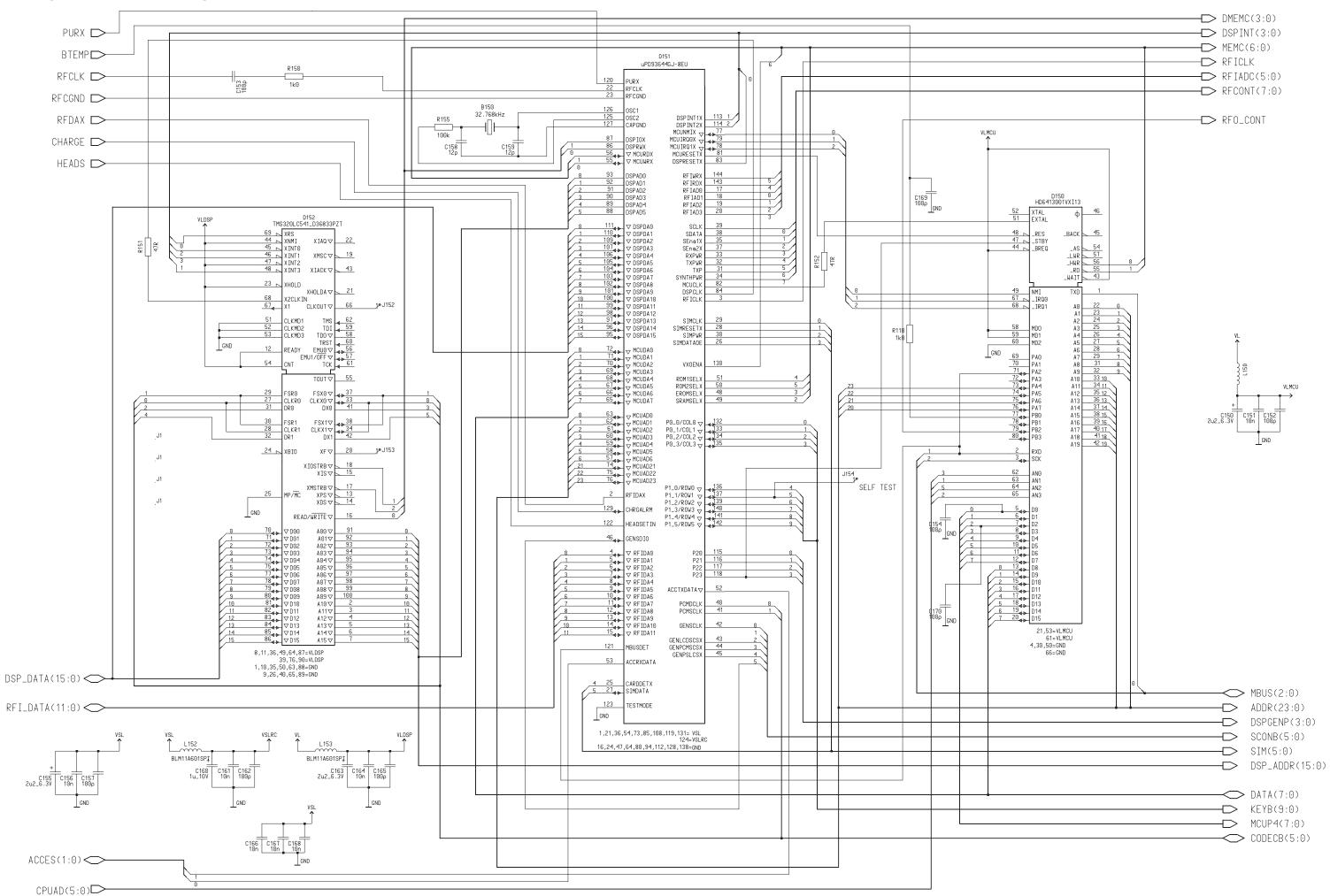


## Figure 31 Audio Circuit Diagram



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#### Figure 32 CPU Circuit Diagram

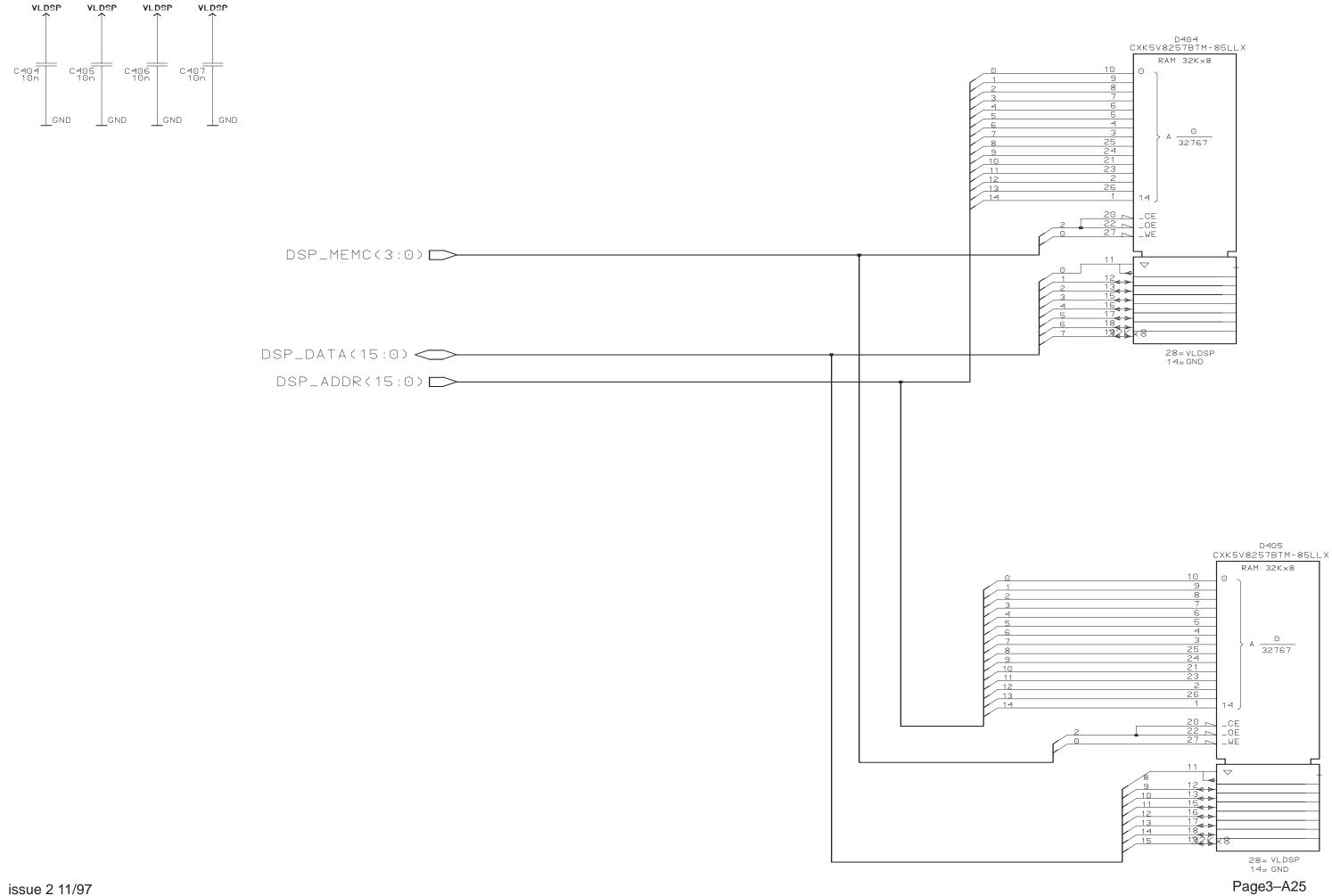


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### NHE 8/9

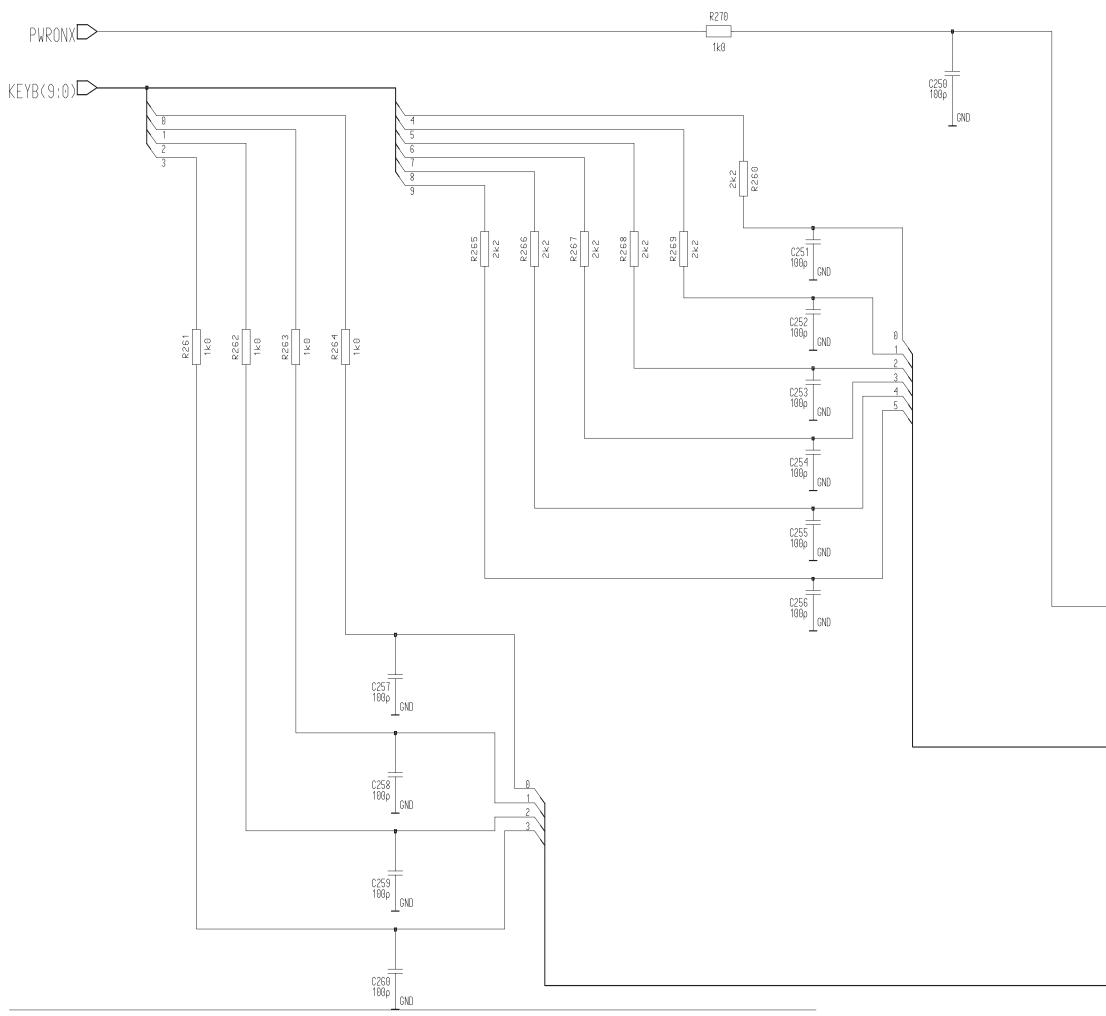
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#### Figure 33 DSP Circuit Diagram





## Figure 34 Keyboard / Display Interface

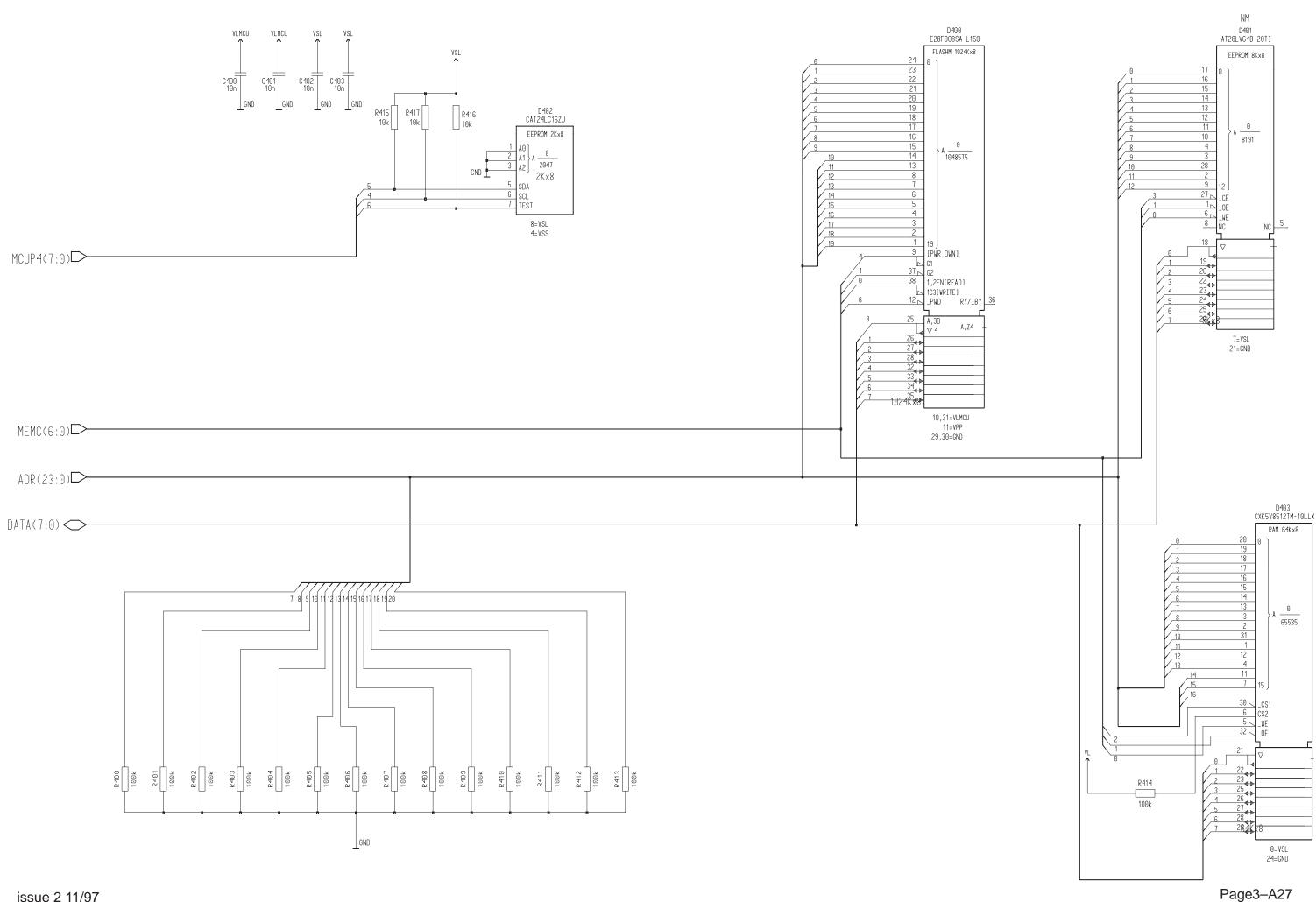


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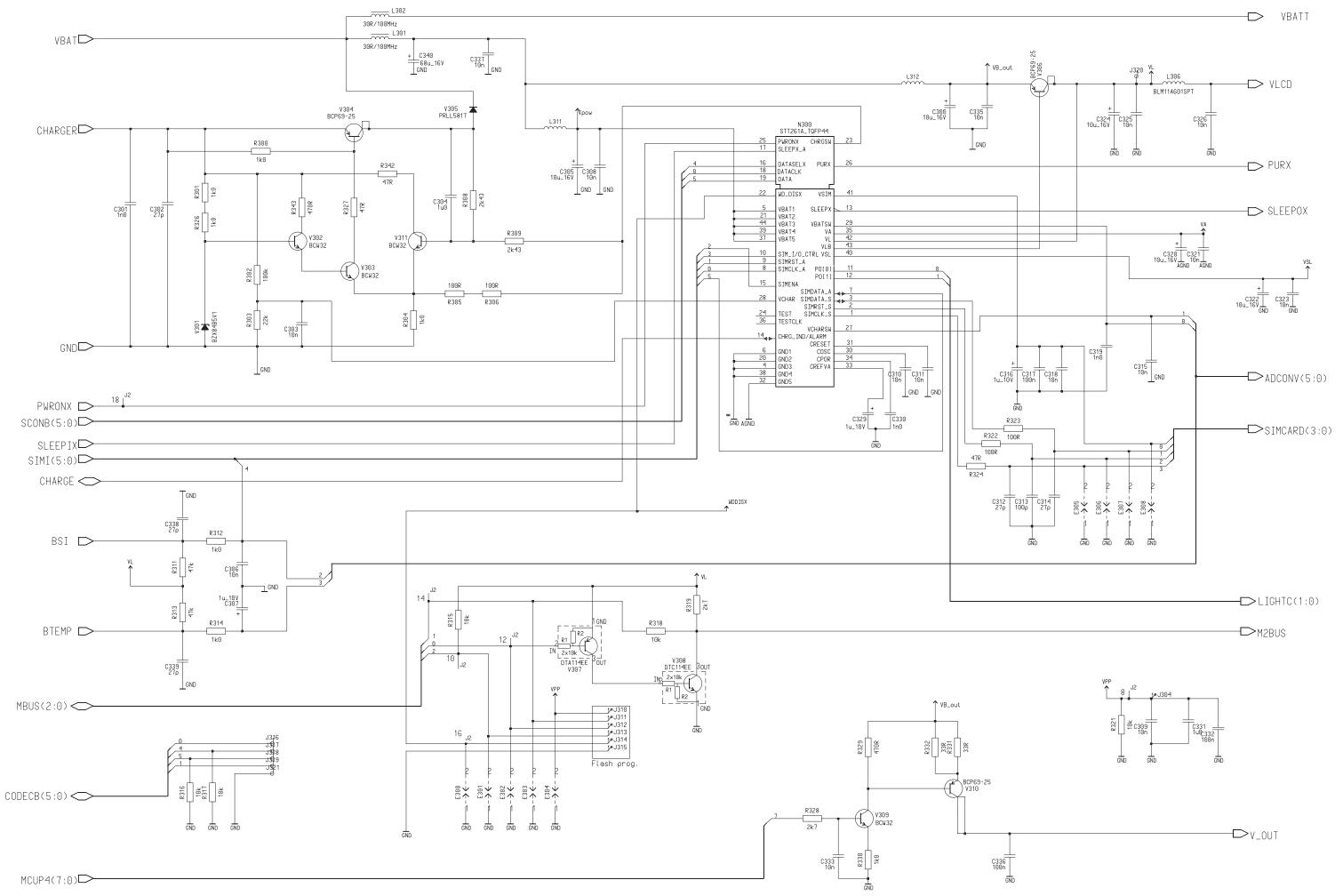




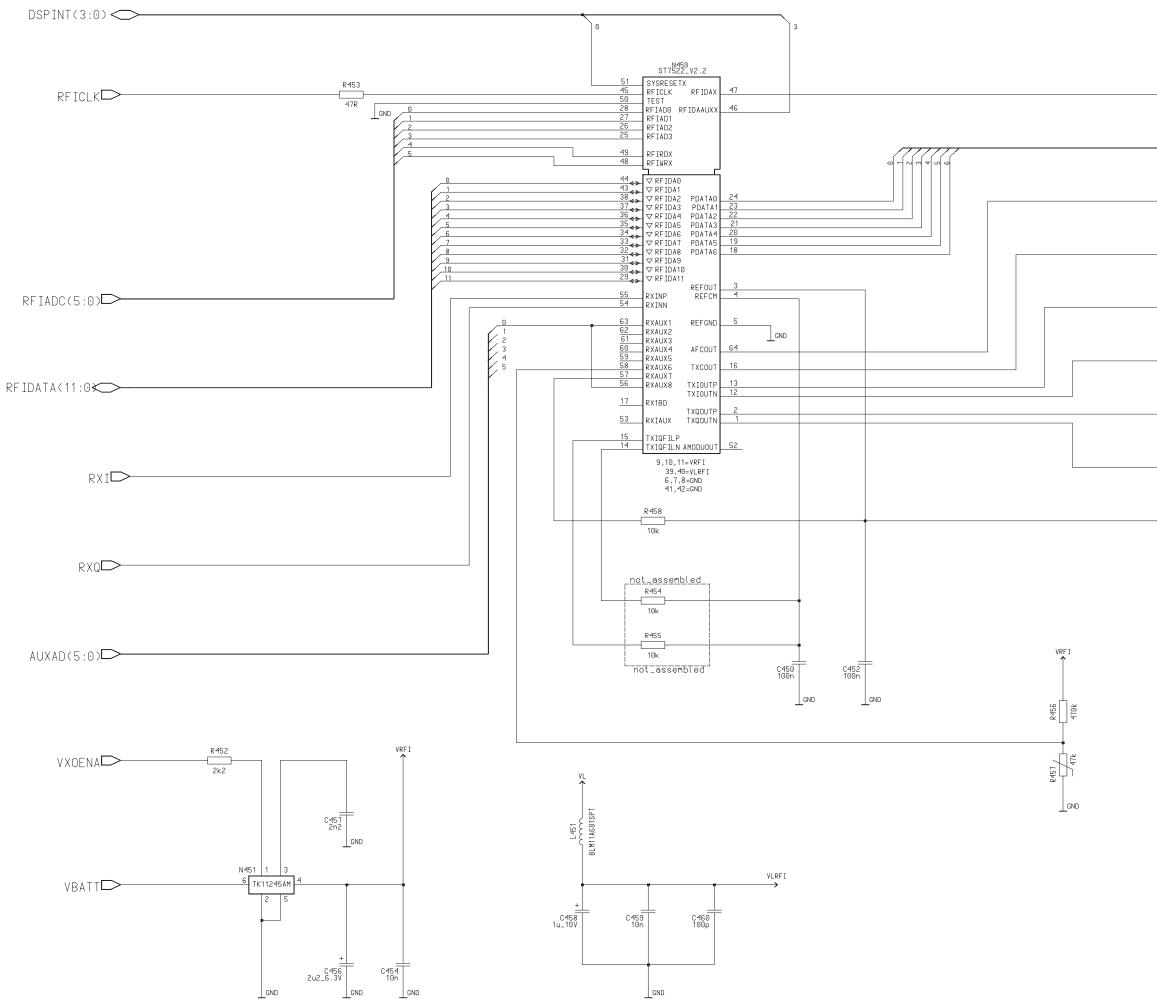








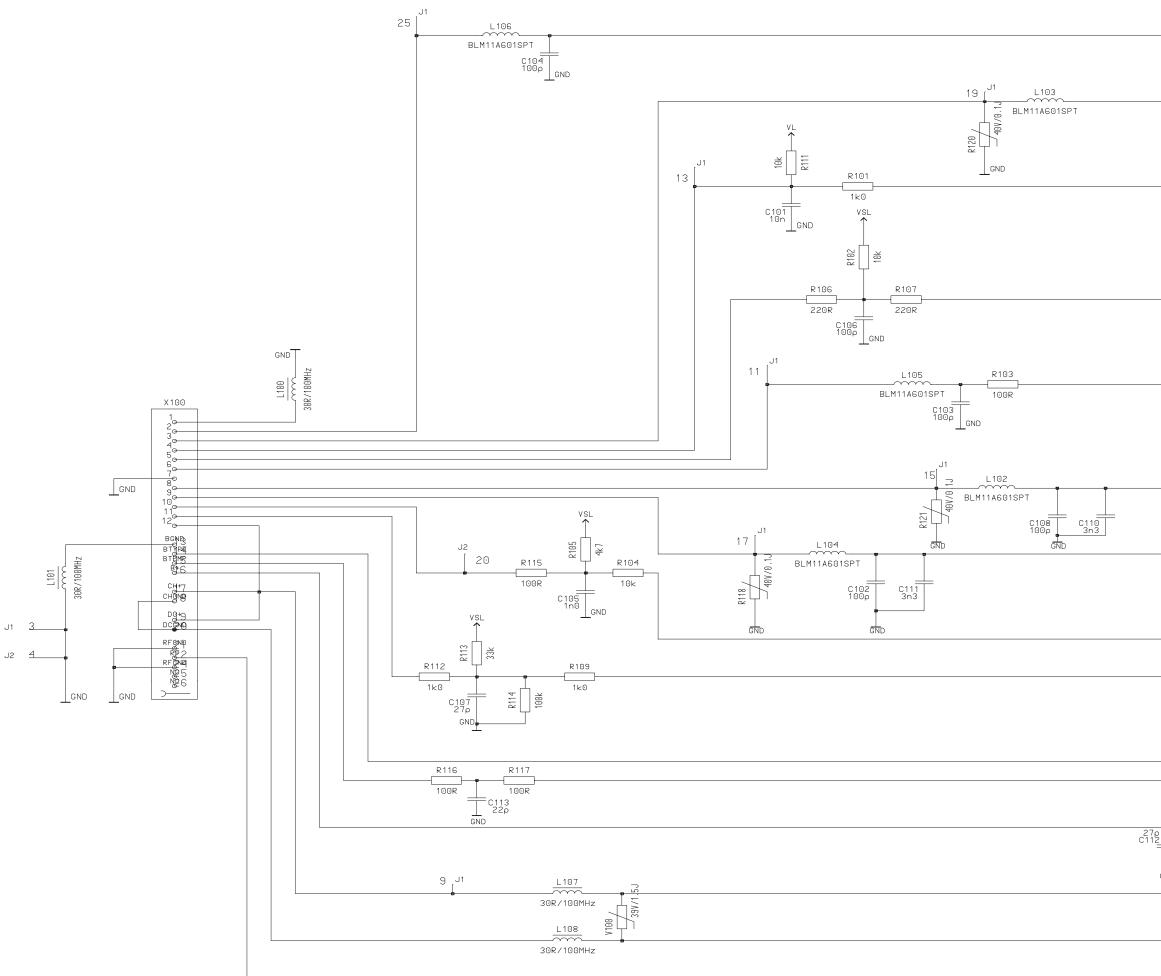
## Figure 37 BB / RF Analog Interface



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——————————————————————————————————————
>TXC
-DTXIP
-DTXIN
-DTXQP
— DTXQN

## Figure 38 System Connector Circuit Diagram



	V_OUT
	XMIC_ID
	EXT_RF
	——————————————————————————————————————
	SGND
	XEAR_MUTE
	——————————————————————————————НООК ——————————
7 J <sup>1</sup> 5 J <sup>1</sup>	BTYPE BTEMP
21 J1	VBAT
ц айр	CHARGER+
	GND