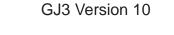
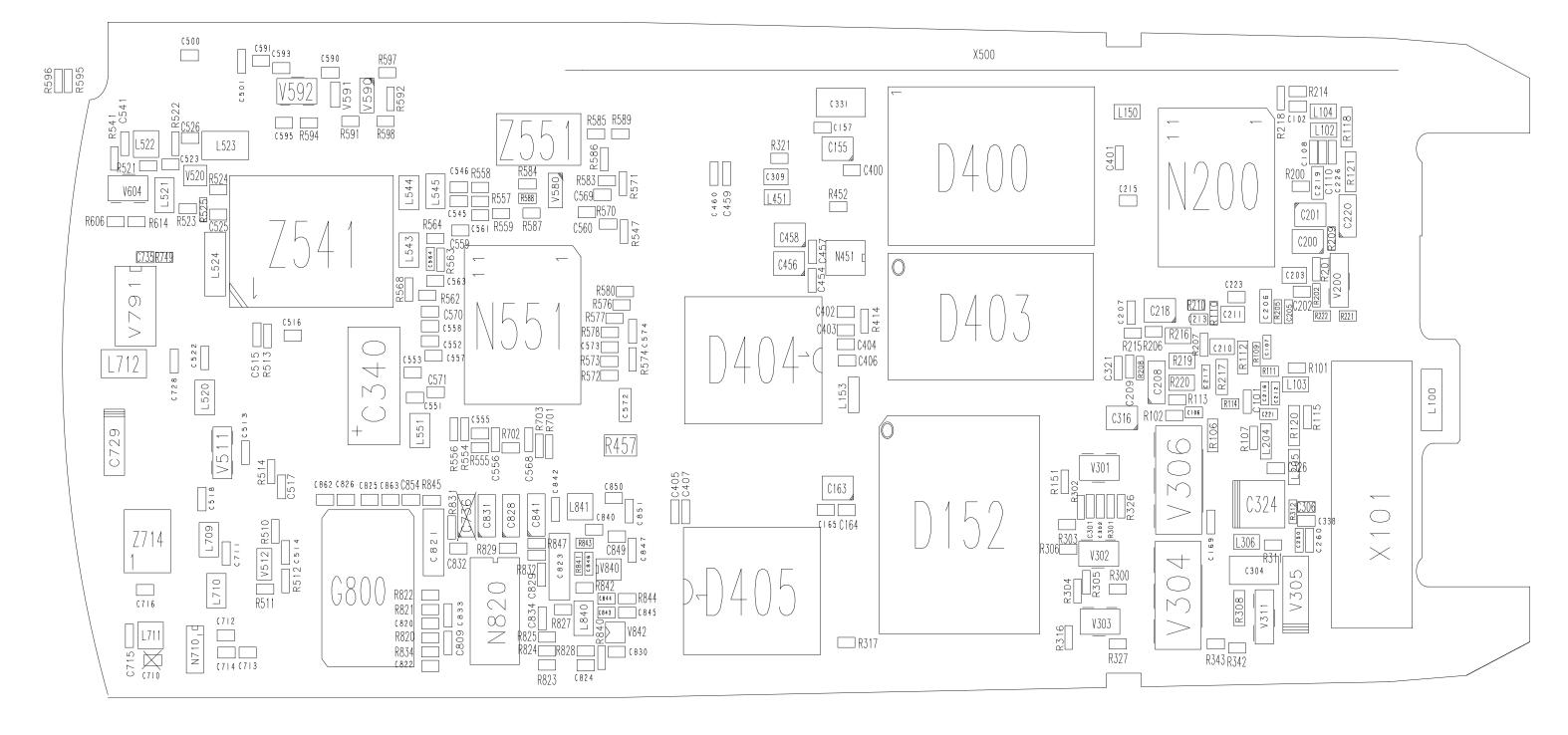
Figure 24 Component Layout Top







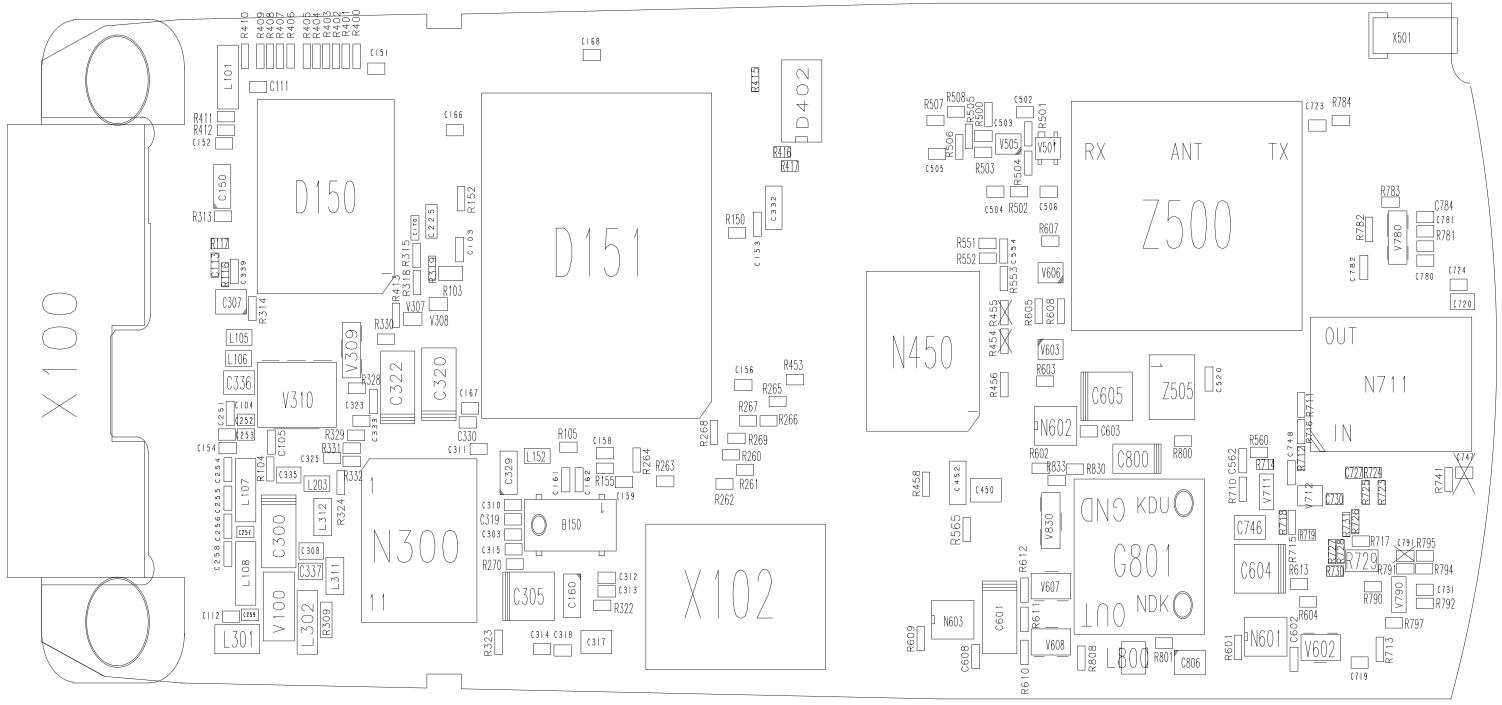


Figure 26 System Block Diagram

Figure 26 System Block Diag	Iram	RFC RXI
		RXQ
SY	Ś	
RXQ RXI		
RFC		AFC TXC TXQP
	T X Q P T X Q N T X I P	TXQN TXIP
	TXIN	SCI K
	SCLK SDATA SENA1	SDATA SENA1
	RXPWR TXPWR	TXPWR TXPWR
	TXP	TXP Synthpwr
	RFOUT	
	PDATAO	PDATAO
	VXOENA	VBATT
	VBATT VREF_2.5V	
	RFO_CONT	VXOENA VBATT VREF_2.5V

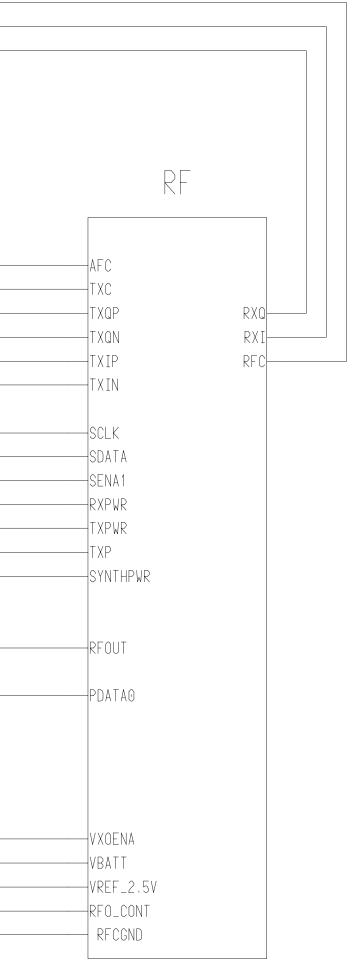
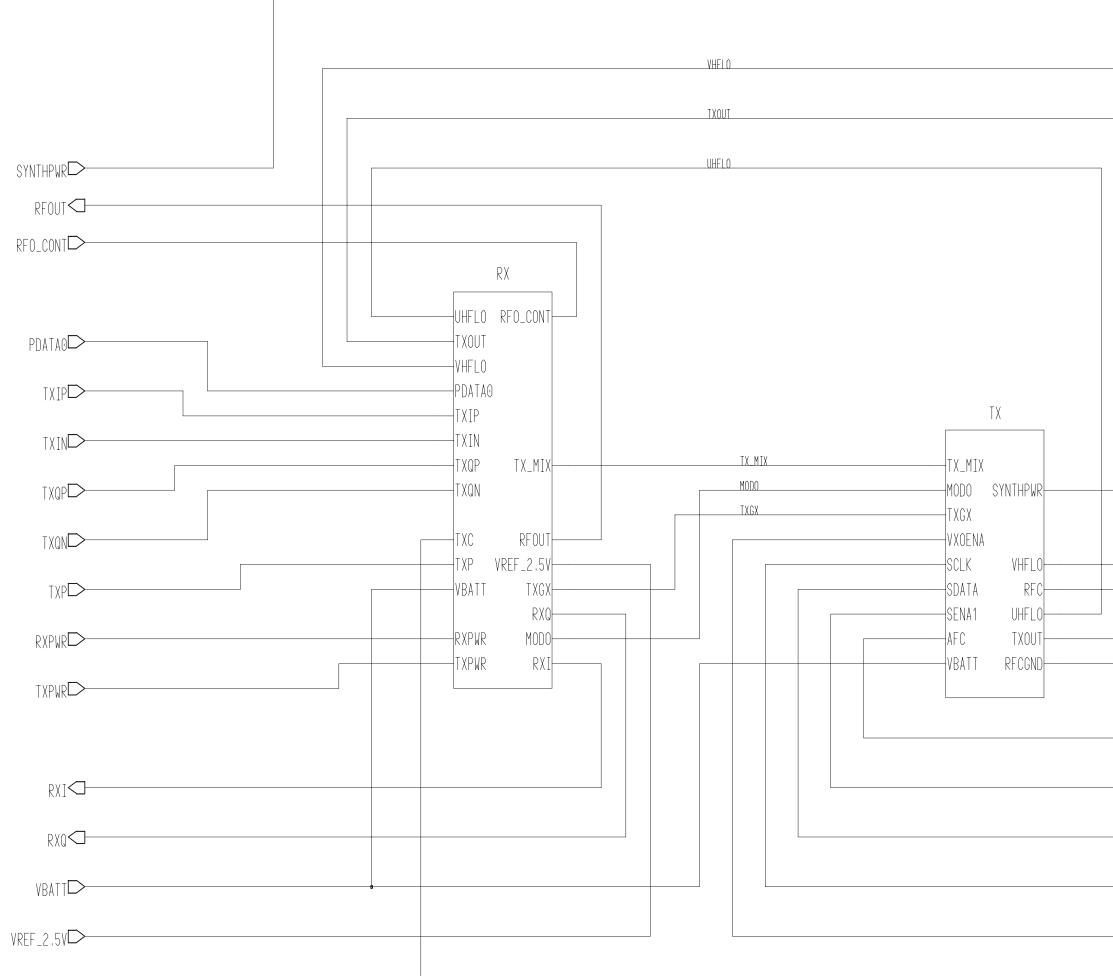
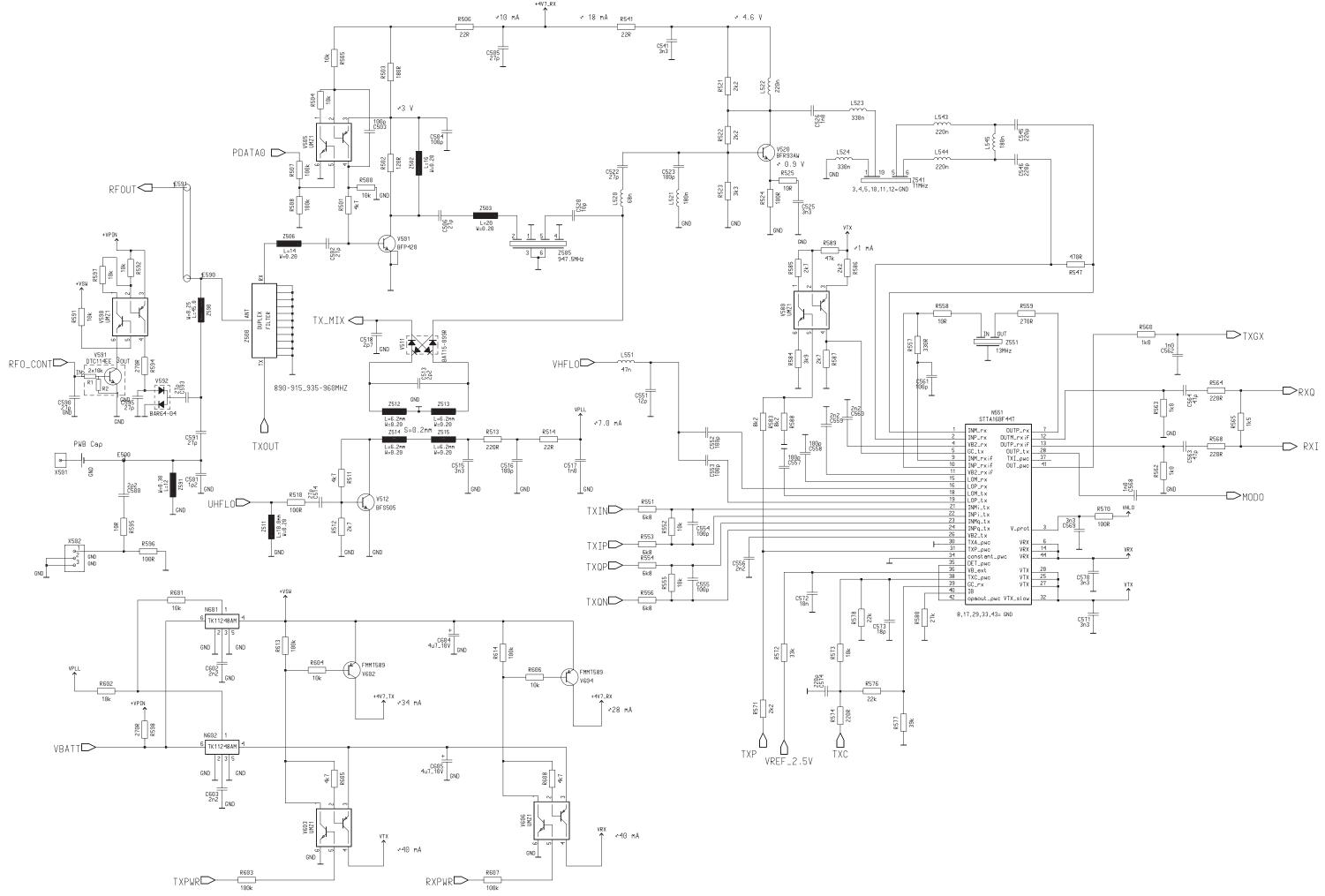


Figure 27 RX/TX Block Diagram



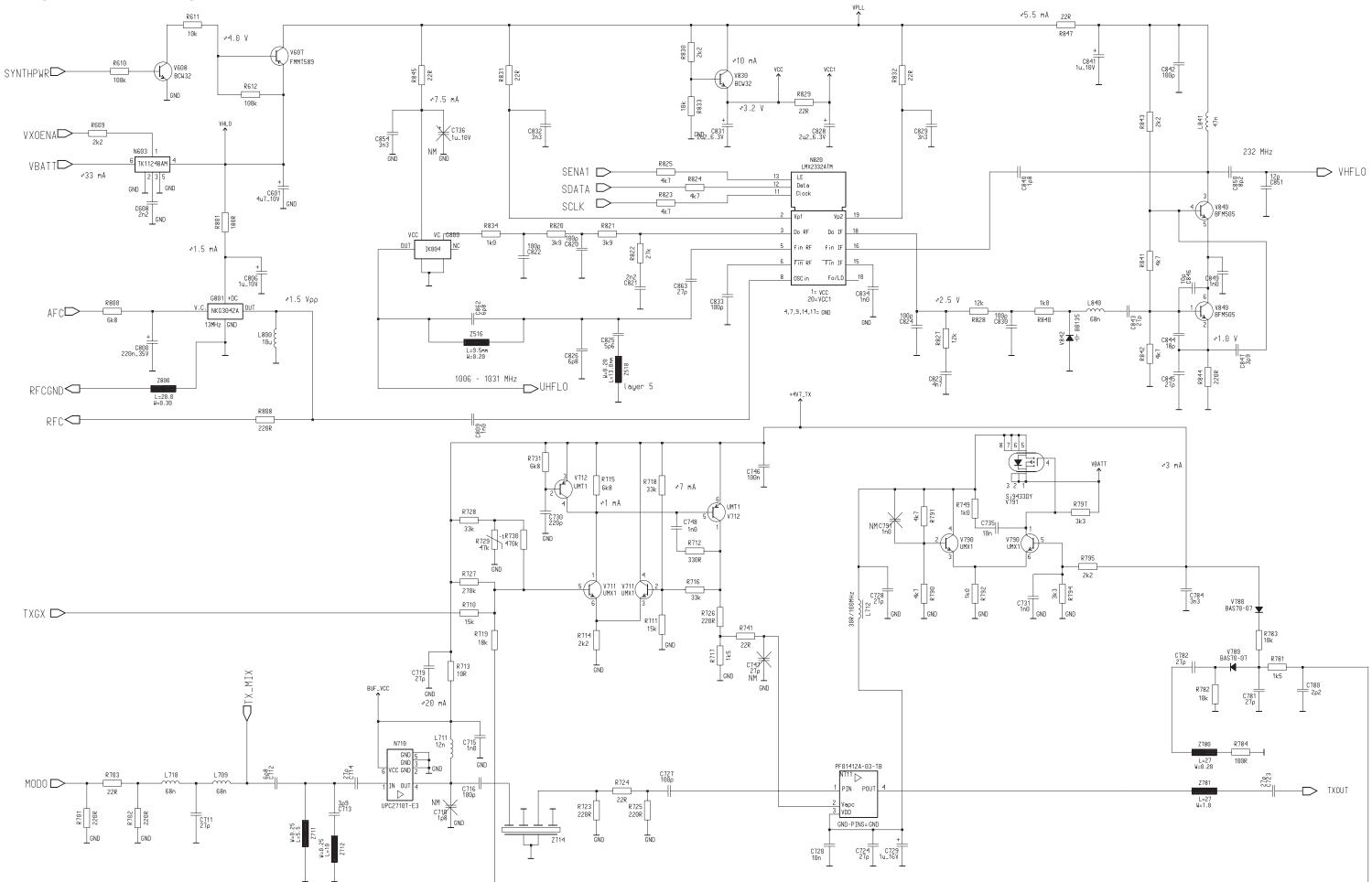
NHE 8/9
RFCGND
SENA1
SDATA
 SCLK
 ۲۸۵ Page3–A19

Figure 28 RX Circuit Diagram

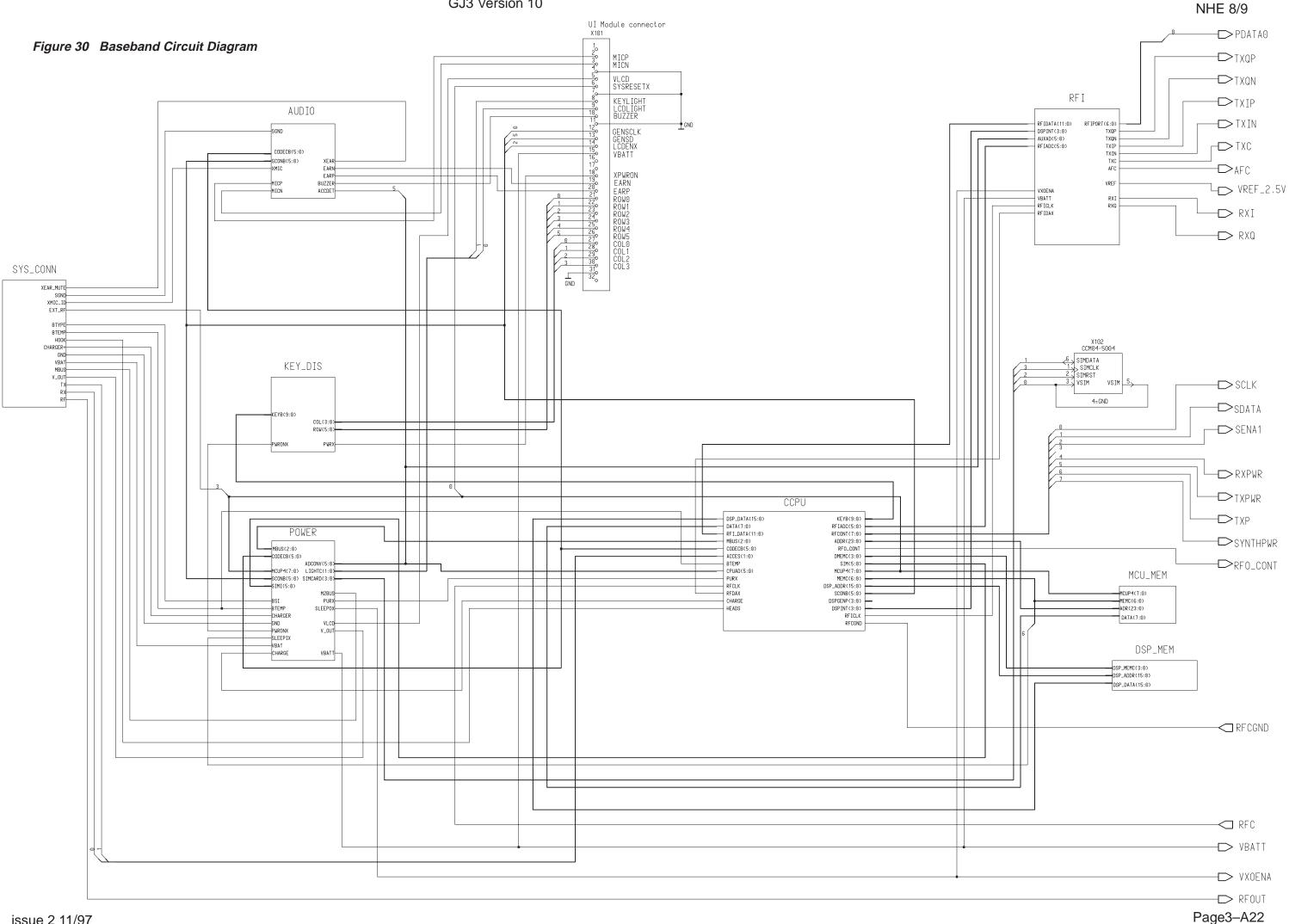


issue 2 11/97

Figure 29 TX Circuit Diagram



NM = NOT MOUNTED

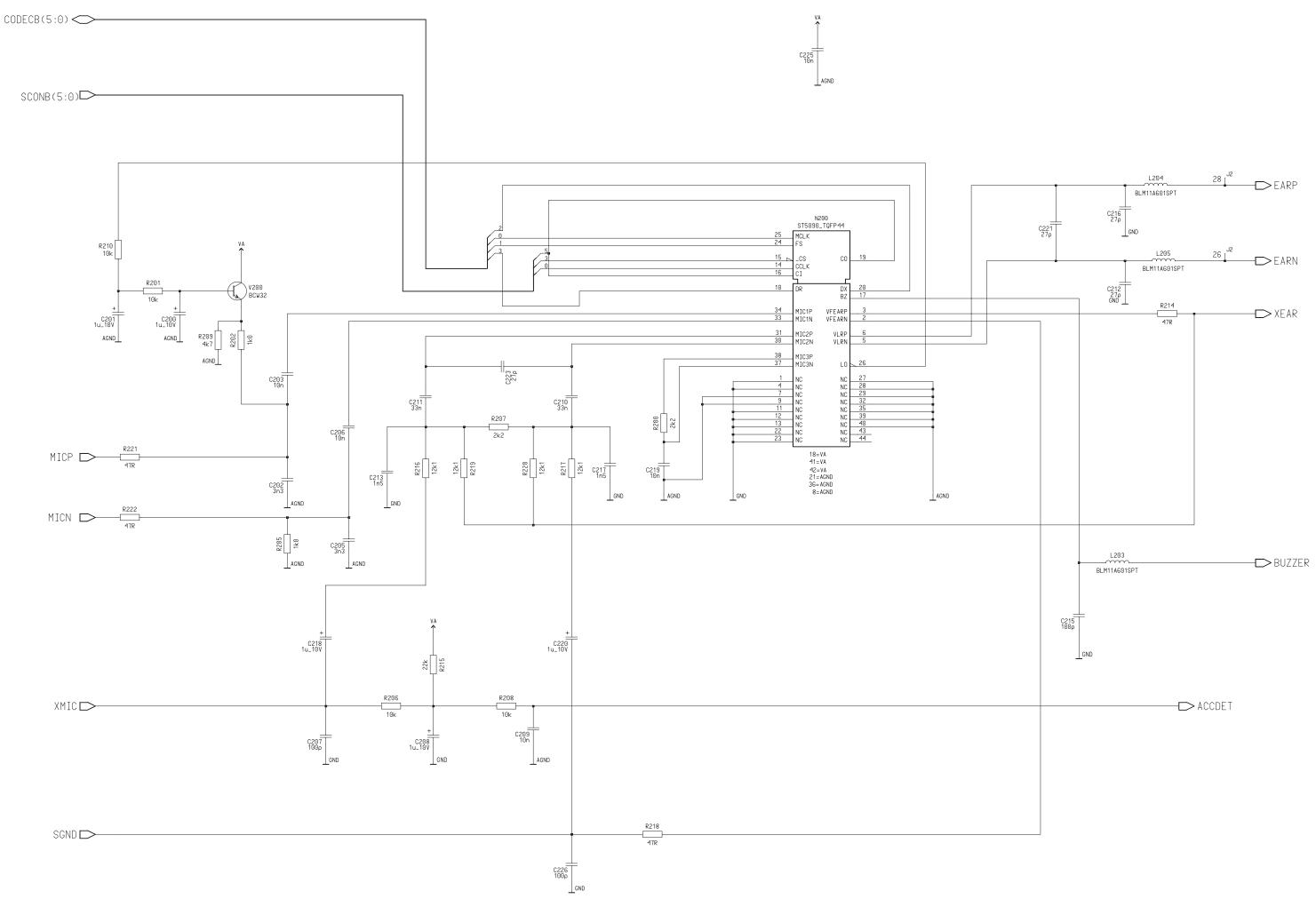


issue 2 11/97

GJ3 Version 10

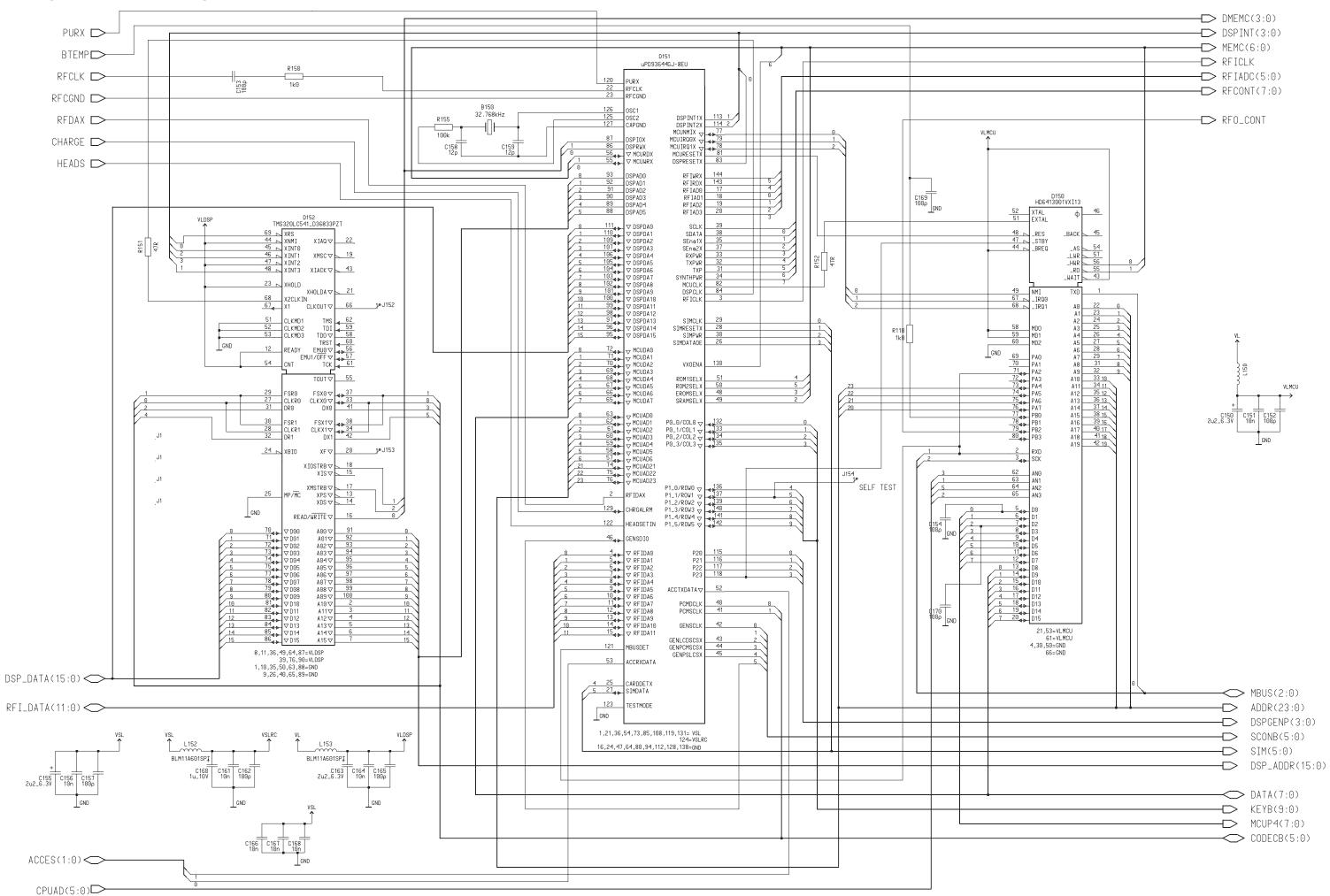


Figure 31 Audio Circuit Diagram



Page3–A23

Figure 32 CPU Circuit Diagram



issue 2 11/97

NHE 8/9

Page3-A24

Figure 33 DSP Circuit Diagram

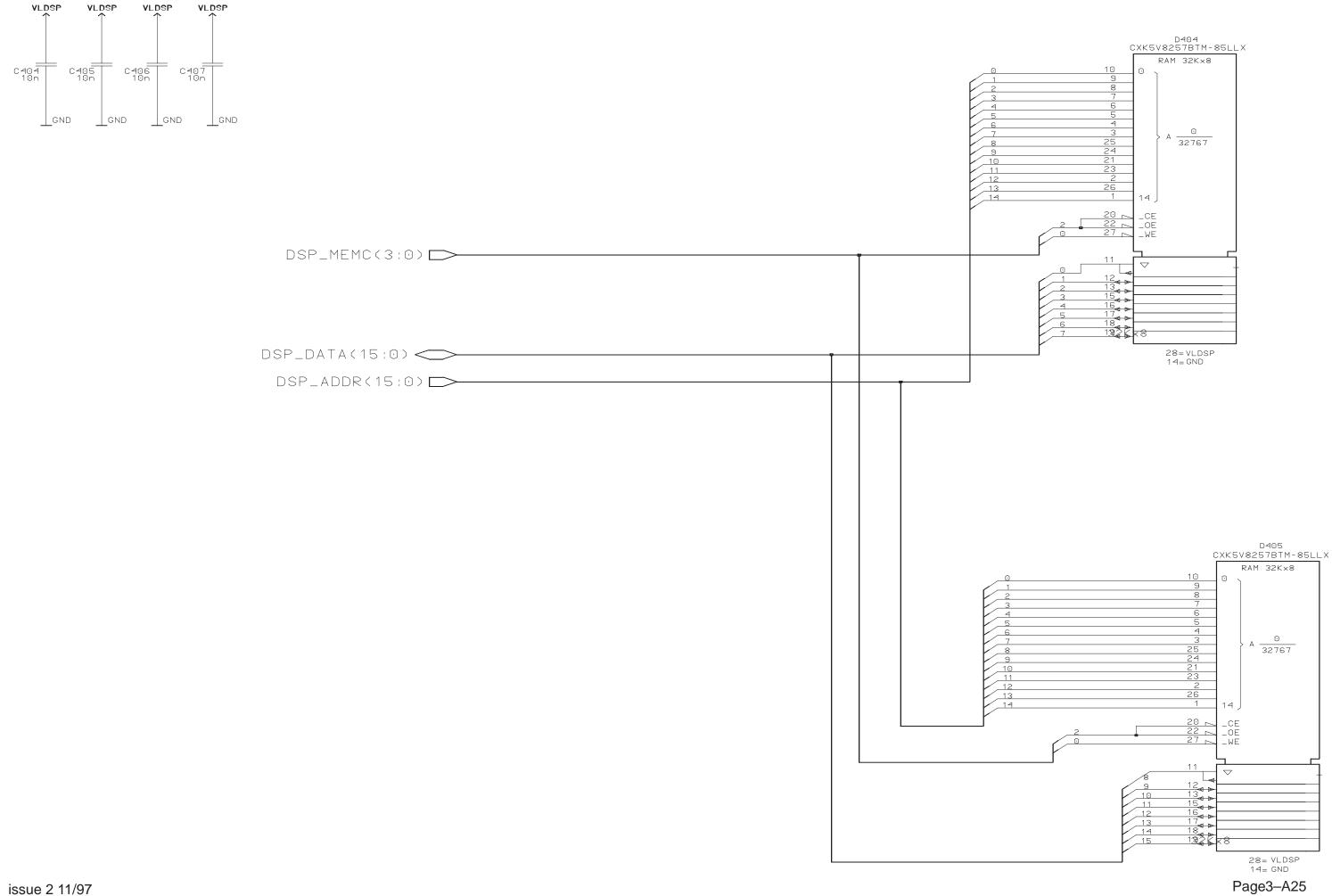
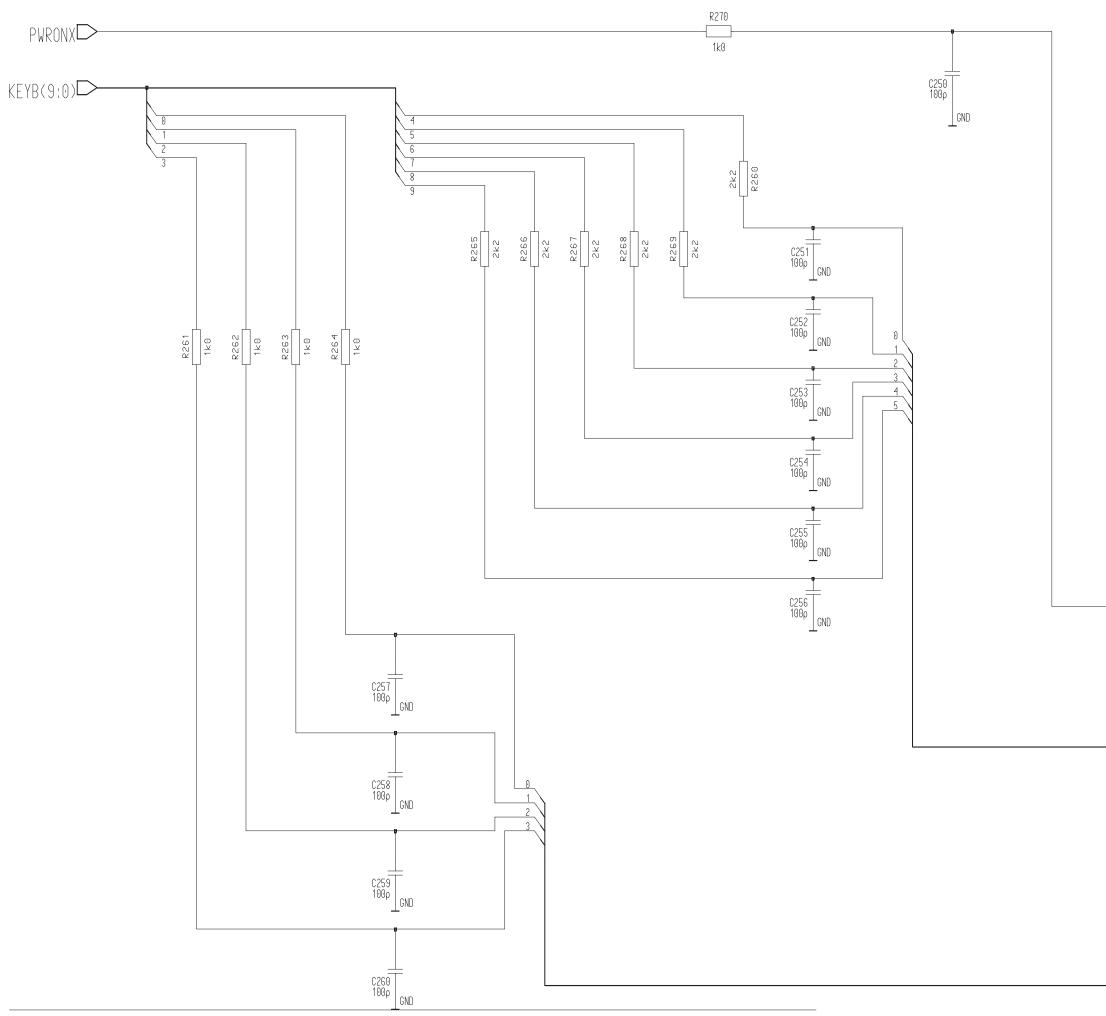




Figure 34 Keyboard / Display Interface

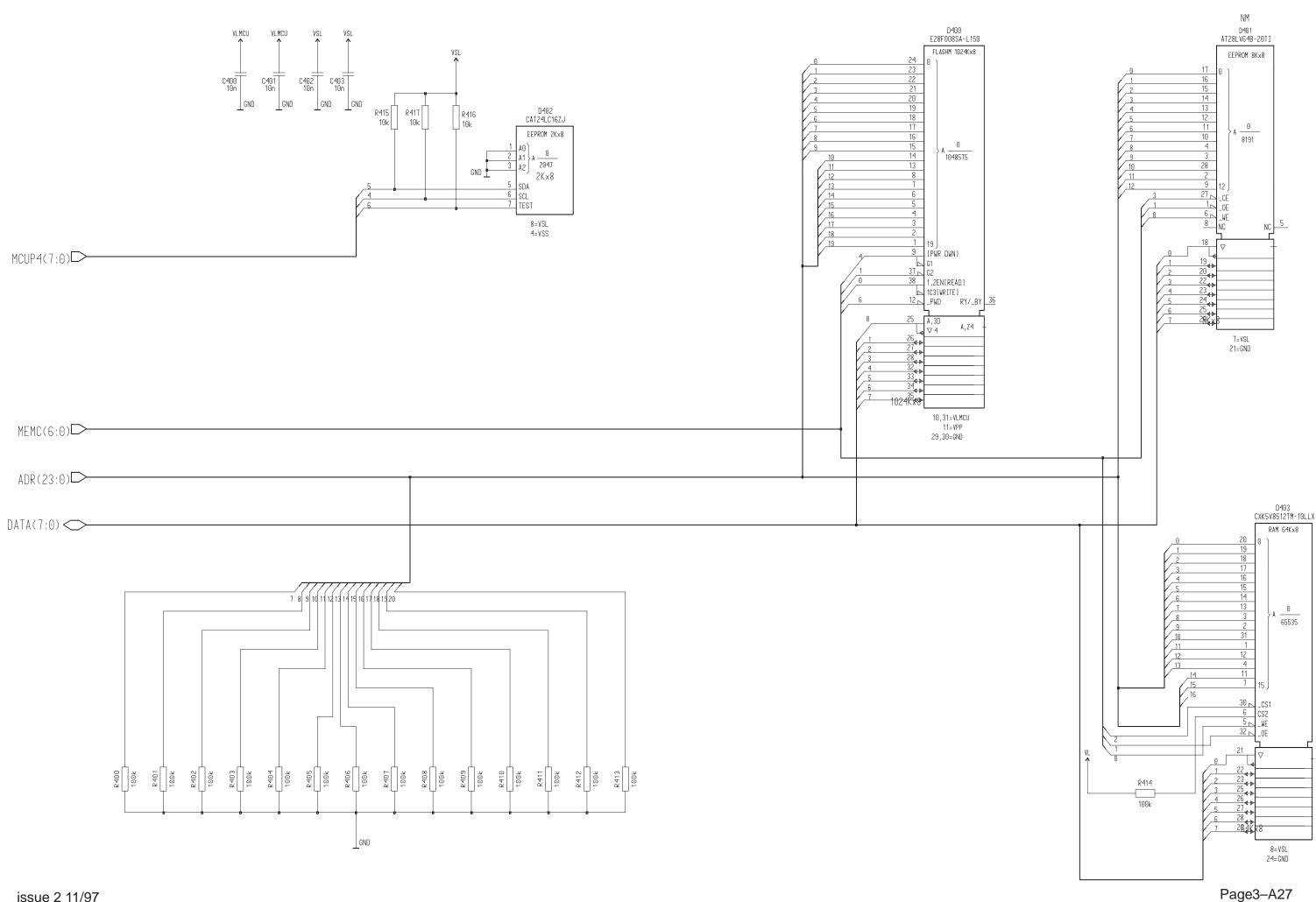


Page3–A26











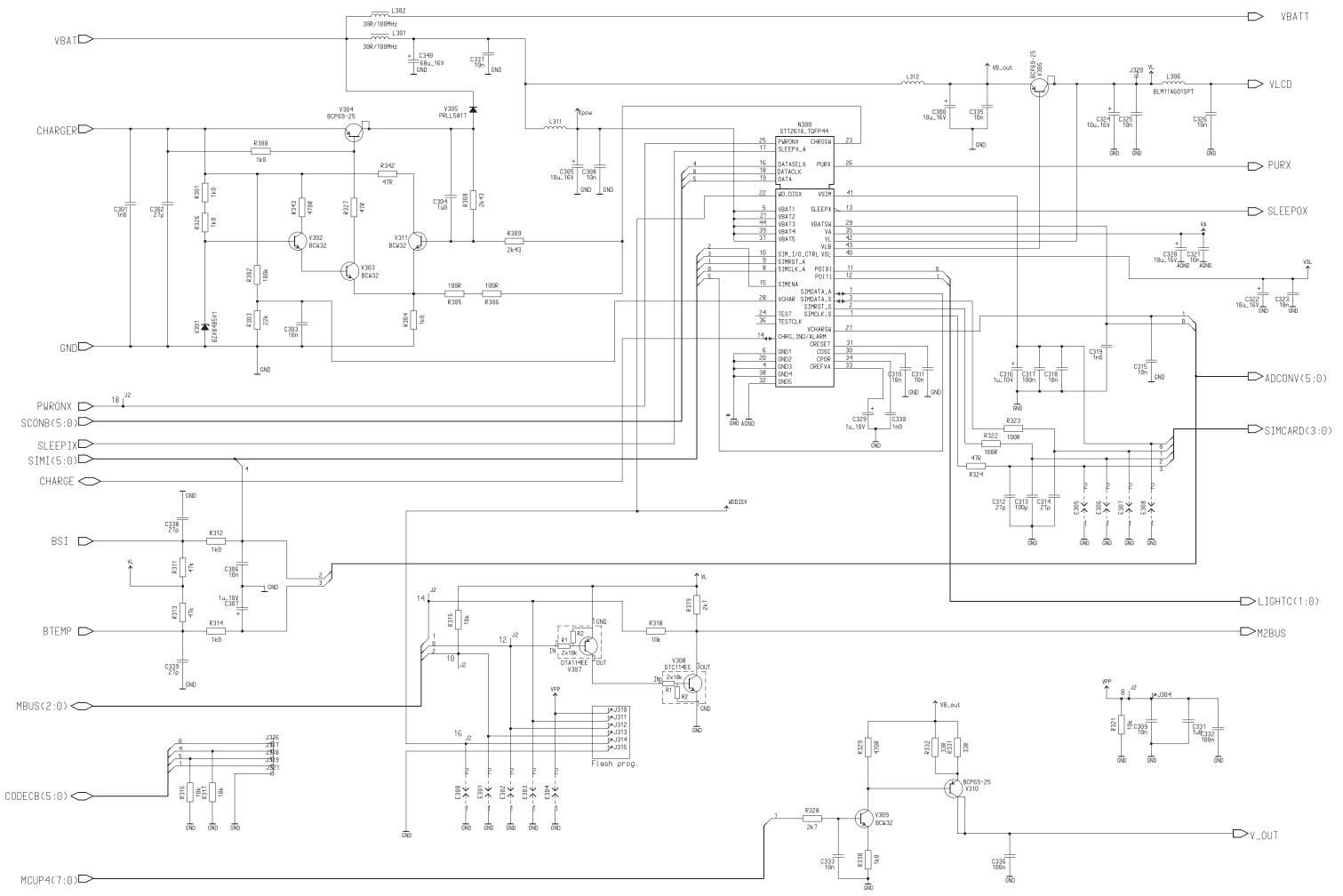
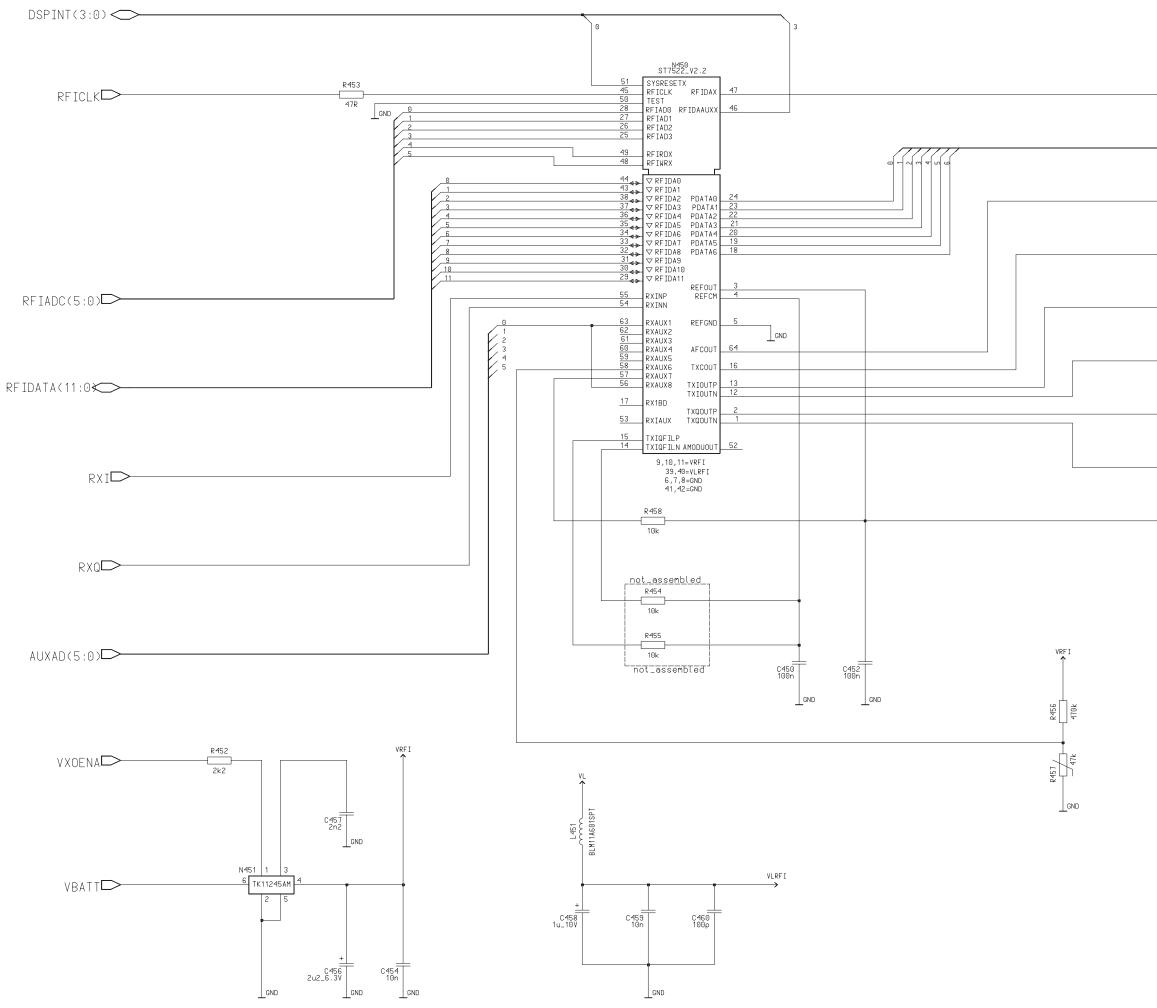


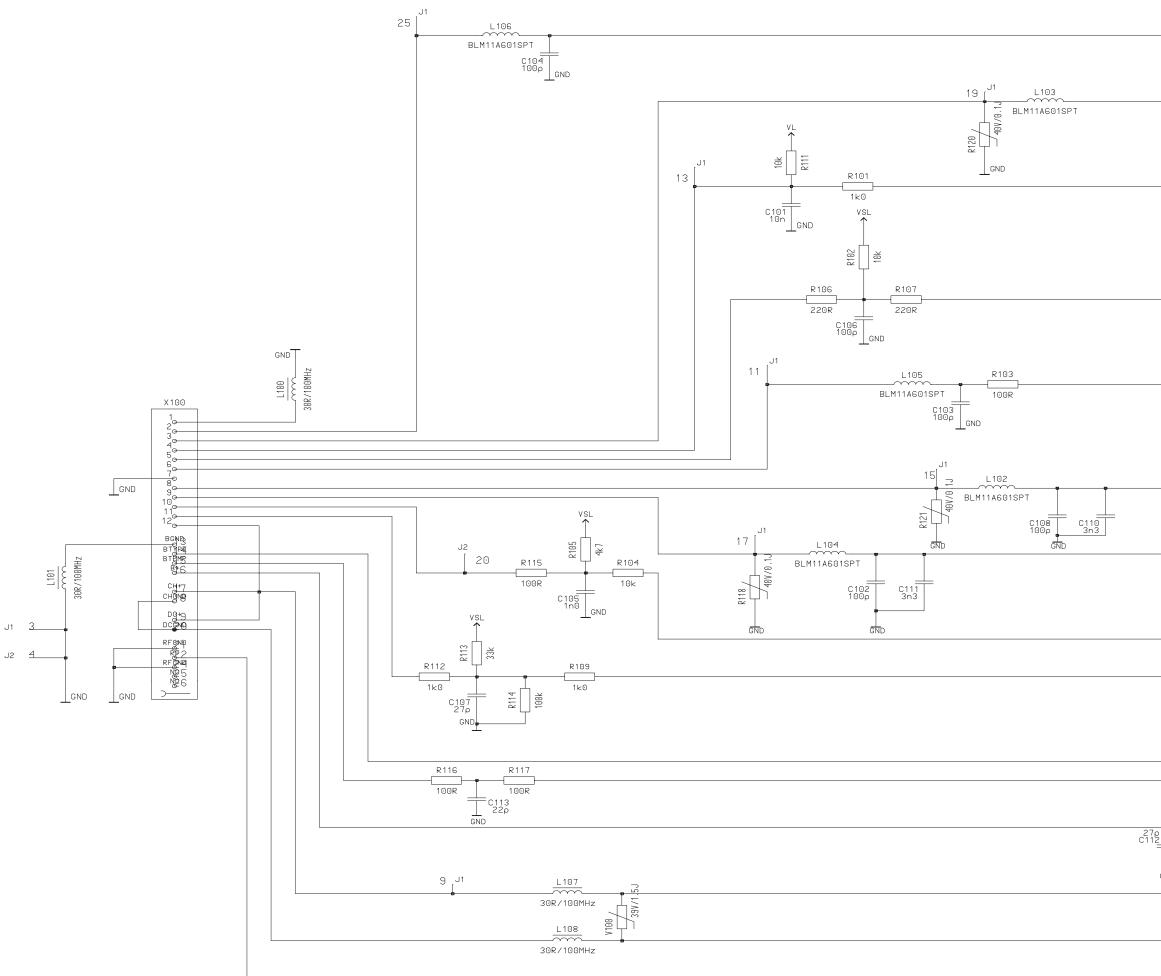
Figure 37 BB / RF Analog Interface



issue 2 11/97

——————————————————————————————————————
>TXC
-DTXIP
-DTXIN
-DTXQP
— DTXQN

Figure 38 System Connector Circuit Diagram



	V_OUT
	XMIC_ID
	EXT_RF
	——————————————————————————————————————
	SGND
	XEAR_MUTE
	——————————————————————————————НООК ——————————
7 J ¹ 5 J ¹	BTYPE BTEMP
21 J1	VBAT
ц айр	CHARGER+
	GND